Voltage Tracking of a DC-DC Buck-Boost Converter Using Neural Network Control

W.M.Utomo, Z.A. Haron, A. A. Bakar, M. Z. Ahmad and Taufik

Abstract—This paper proposes a neural network control scheme of a DC-DC Buck-Boost converter to produce variable DC voltage source that will be applied on DC motor drives. In this technique, a back propagation learning algorithm is derived. The controller is designed to track the output voltage of the DC-DC converter and to improve performance of the Buck-Boost converter during transient operations. Furthermore, to investigate the effectiveness of the proposed controller, some operations such as starting-up and reference voltage variations are verified. The numerical simulation results show that the proposed controller has a better performance compare to the conventional PI-Controller method.

Index Terms—Buck-Boost converter, neural network controller.

I. INTRODUCTION

In recent pass, DC power supplies are extensively utilized in many areas compromising of simple electronic devices such as notebook computers, till even more advance application such as electric vehicle and also the aerospace applications. Hence, the DC-DC converter is widely used by convert a DC voltage to a different DC voltage level in order to provide the DC voltage source level requirements of the load to the DC power supply. In addition, the DC-DC converter is also an important application for the power conditioning of the alternative electrical energy such as photovoltaic, wind generator and fuel cell system. Due to these reasons, the DC-DC converter application will head to a more potential market in the future.

Basically, the DC-DC converter consists of the power semiconductor devices which are operated as electronic switches and classified as switched-mode DC-DC converters or normally refers as Switched mode power supply (SMPS). Operation of the switching devices causes the inherently nonlinear characteristic of the Buck-Boost converters. Due to this unwanted nonlinear characteristics, the converters requires a controller with a high degree of dynamic response. Recently, the research on the switching control techniques has been highlighted in order to achieve a high-quality power system. Pulse Width Modulation (PWM) is the most frequently consider method among the various switching control method [1].

In the past decade, the controller for the PWM switching control is restraining to Proportional-Integral-Differential (PID) controller. This controller often applied to the converters because of their simplicity. However, implementations of this control method to the nonlinear plants such as the power converters will undergo from dynamic response of the converter output voltage regulation. In general, PID controller produces long rise time when the overshoot in output voltage decreases [2].

In order to tackle this problem and improve the dynamic response of DC-DC converters, several intelligence controllers such as fuzzy logic control, neural network control and hybrid neuro-fuzzy control methods for DC-DC converter have been reported in [3]-[8]. The purpose and utilization of the fuzzy controller for dc-dc converter has been developed in [5]. Implementations of the fuzzy logic control to dc-dc converter using micro controller have been verified in [6]. Inherently, the relatively simple fuzzy controller has a good performance for those systems where linear control technique fail and can apply to any dc-dc converter topologies.

Due to lack of formal analysis and synthesis technique [4] it has not been viewed as a rigorous science, even though many practical successes has been achieved by the fuzzy logic controllers, hence, a lot of research has been carried out to improve the control system. A fuzzy-neural sliding-mode (FNSM) control system is one of the approaches that develop to control power electronic converters [3] for a PWM-based power electronic system. The FNSM control system consists of a compensation controller and a neural controller where the compensation robust controller is designed to recover the residual of the approximation error, while the neural controller is designed to approximate an ideal controller. An Adapt recurrent fuzzy neural network (ARFNN) control system is proposed in [4] in order to achieve good regulation performances. With the on-line learning algorithm is applied, the ARFNN control scheme is suitable to control the dc-dc converter.

Neural network controls (NNC) as another type of intelligence controls has ability to approach any function by learning process, hence this controller is suitable for nonlinear control system. NNC for the DC-DC has been tested in both laboratory [8] and computer simulation model [9]-[10], both turned out to be successful. NNCs illustrate the result in minimizing the difference between the output voltage and the reference voltage [9]. In addition, the dynamic characteristics of dc-dc converter are improved and can realize excellent dynamic characteristics with the used of neural network predictor [10] as compare with the conventional one.
In order to improve performance of the NNC some researchers have been done to develop online learning scheme of the NNC.

The organization of this paper is as follows: Section II discusses basis concept of a Buck-Boost converter as a step-up and step-down of a DC-DC converter. In section III, the design of neural network control is described. Simulation results are carried out in section IV. Finally, conclusions are summarized in Section V.

II. BUCK-BOOST CONVERTER

The Buck-Boost converter is a type of step-down and step-up DC-DC converter. Output of the Buck-Boost converter is regulated according to the duty cycle of the PWM input at fixed frequency. When the duty cycle (d) is less than 0.5, the output voltage of converter is lower than the input voltage. On other condition, when the duty cycle is more than 0.5 the output voltage of converter is higher than the input voltage. The basic circuit of a Buck-Boost converter is illustrated in Fig.1 where $V_i$ is input voltage source, $V_o$ is output voltage, $S_w$ is switching component, $d$ is diode, $C$ is capacitance, $L$ is inductor windings and $R$ is load resistance.

![Circuit diagram of a Buck-Boost converter](image)

The equivalent equations for Buck-Boost converter during switching-on can be derived as follow:

When the switch is ON, the diode is open

$$v_i = v_s$$  \hspace{1cm} (1)

$$v_l = L \left( \frac{di}{dt} \right)$$  \hspace{1cm} (2)

Substitutions of (1) and (2)

$$v_s = L \left( \frac{di}{dt} \right)$$  \hspace{1cm} (3)

$$\Delta i_{(close)} = \frac{v_s DT}{L}$$  \hspace{1cm} (4)

When the switch is OFF, the diode is closed

$$v_i = v_o$$  \hspace{1cm} (5)

$$v_L = L \left( \frac{di}{dt} \right)$$  \hspace{1cm} (6)

Substitutions of (5) and (6)

$$L \left( \frac{di}{dt} \right) = v_o$$  \hspace{1cm} (7)

$$\Delta i_{(open)} = -\frac{v_o (1-D)T}{L}$$  \hspace{1cm} (8)

where $D$ is duty cycle.

In steady state operation by solving the linear equation during turn-on and turn-off the average output voltage is derived as follow

$$\Delta i_{(open)} + \Delta i_{(close)} = 0$$

$$\frac{v_s DT}{L} + \frac{v_o (1-D)T}{L} = 0$$

$$\frac{v_o (1-D)T}{L} = -\frac{v_s DT}{L}$$

$$v_o = -\frac{v_s D}{1-D}$$  \hspace{1cm} (9)

To develop a dynamic model of the Buck-Boost converter, a state space averaging model is applied. In this method the averaging state space formula of the converter during turn-on and turn-off with $D=1$-$D$ where $d$ is duty cycle are given as

$$\dot{X} = AX + BU$$  \hspace{1cm} (10)

$$Y = CX$$  \hspace{1cm} (11)

where

$$X = \begin{bmatrix} i_L \\ v_C \end{bmatrix}$$

$$U = v_1$$

$$Y = v_o$$

$$\dot{A} = \begin{bmatrix} 0 & D' \\ -D' & -1 \end{bmatrix} \frac{L}{C}$$

$$B = \begin{bmatrix} 1 \\ 0 \end{bmatrix} \frac{L}{RC}$$

$$C = [0 \ 1]$$
The transfer function of the Buck-Boost converter in the continuous system is finally found as
\[ G(s) = \frac{-D' R}{LRCs^2 + Ls + D'^2 R} \]  
(12)

III. NEURAL STRUCTURES AND LEARNING SCHEME

A. Structure of Neural Network Controller

To design the neural network control, some information about the plant is required. Basically, the numbers of input and output neuron at each layer are equal to the number of input and output signals of the system respectively. The structure of the proposed neural network control of a Buck-Boost converter is as shown in Fig.2.

Based on the number of neurons in each layer of the proposed NNC architecture, the network has a 1-3-1 neurons structure. In the input layer consists of an input neurons. The first input neuron is error signal between desired signal and actual signal.

The connections weight parameter between \( j \)th and \( i \)th neuron at \( m \)th layer is given by \( w_{ij} \), while bias parameter of this layer at \( i \)th neuron is given by \( b_i^m \). Transfer function of the network at \( k \)th neuron in \( m \)th layer is defined as
\[ n_i^m = \sum_{j=1}^{S_m} w_{ij}^m a_j^{m-1} + b_i^m \]  
(13)

The output function of neuron at \( m \)th layer is given by
\[ a_i^m = f^m(n_i^m) \]  
(14)

Where \( f \) is activation function of the neuron. In this design, the activation function for the output layer and the hidden layer are unity and a tangent hyperbolic function respectively.

The activation function of the hidden layer is given as
\[ f^m(n_i^m) = \frac{2}{1 + e^{-2n_i^m}} - 1 \]  
(15)

Updating of the connection weight and bias parameters are given by
\[ w_{ij}^m(k + 1) = w_{ij}^m(k) - \alpha \frac{\partial F(k)}{\partial w_{ij}^m} \]  
(16)

\[ b_i^m(k + 1) = b_i^m(k) - \alpha \frac{\partial F(k)}{\partial b_i^m} \]  
(17)

where \( k \) is sampling time, \( \alpha \) is learning rate, and \( F \) performance index function of the network

B. Learning Algorithm of Back Propagation

After the neural network architecture is modeled, the next stage defines the learning model to update network parameters. By this learning capability, it makes the ANN suitable to be implemented for the system with motor parameters which are difficult to define and vary against with environment. The training process minimizes the error output of the network through an optimization method. Generally, in learning mode of the neural network controller a sufficient training data input-output mapping data of a plant is required. Since the motor parameters of the induction motor drive vary with temperature and magnetic saturation, the online learning Back propagation algorithm is developed.

Based on the first order optimization scheme, updating of the network parameters is covered. The performance index sum of square error is given by
\[ F(k) = \frac{1}{2} \sum_i e_i^2(k) \]  
(18)

\[ e_i(k) = t_i(k) - a_i(k) \]  
(19)

where \( t_i \) is target signal and \( a_i \) output signal on last layer. The gradient descent of the performance index against to the connection weight is given by
\[ \frac{\partial F}{\partial w_{ij}^m} = \frac{\partial F}{\partial n_i^m} \frac{\partial n_i^m}{\partial w_{ij}^m} \]  
(20)

The sensitivity parameter of the network is defined as
\[ s_i^m = \frac{\partial F}{\partial n_i^m} \]  
(21)
\[
S_i^m = \frac{\partial F}{\partial a_i^m} \frac{\partial a_i^m}{\partial n_i^m}
\]

Gradient the transfer function again to the connection weight parameter is given by

\[
\frac{\partial n_i^m}{\partial w_{ij}^m} = a_i^{m-1}
\]

From substitution equation (21) and (23) into (16) the updating connection parameter is given by

\[
w_i^{m-1}(k + 1) = w_i^{m-1}(k) - \alpha a_i^m(k) a_i^{m-1}(k)
\]

With the same technique the updating bias parameter is given by

\[
b_i^{m-1}(k + 1) = b_i^{m-1}(k) - \alpha a_i^m(k)
\]

IV. SIMULATION RESULTS

A computer simulation using Simulink - MATLAB has been conducted to predict the effectiveness of the proposed controller [10]. Block diagram of the proposed NNC for the Buck-Boost converter is shown in Fig.3. The Buck-Boost converter parameters are shown in Table 1.

The proposed NNC produce a better performance than PI-Controller such as removing overshoot and oscillation to achieve desired output voltage as shown in Fig. 4 and 5. Furthermore, the settling time of the NNC is faster than PI-Controller.

Fig.6 and 7 shows the output voltage transient responses of the Buck-Boost converter to the change of reference voltage. In Fig.6, the reference voltage stepping-up from 12 to 24 volt. In Fig.7, the reference voltage stepping-down from 24 to 12 volt.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>L</td>
<td>Inductance</td>
<td>0.6 (mH)</td>
</tr>
<tr>
<td>C</td>
<td>Capacitance</td>
<td>100 (μF)</td>
</tr>
<tr>
<td>(V_1)</td>
<td>Input voltage</td>
<td>12 (volt)</td>
</tr>
</tbody>
</table>

The comparison between conventional PI controller and the proposed NNC is done in this simulation. It is found that the output voltage startup transient response of the Buck-Boost converter with reference voltage is higher than the input voltage source as in the case of the boost converter (the output voltage at 24 volt) and lower than the input voltage source as in the case of the buck converter (the output voltage 12 volt) as shown in Fig.4 and Fig.5 respectively.
In this paper a neural network control for Buck-Boost DC-DC converter is discussed. To enhance the performance of the neural network controller, an algorithm based on back propagation scheme is implementing. The implementation of the online learning technique is feasible for the Buck-Boost converter based on the results in the simulation. It is observable that the NNC is effective in decreasing overshoot as well as oscillation and settling time and also has a fast response to track desired output voltage.

ACKNOWLEDGMENT

The authors would like to gratitude Univeriti Tun Hussein Onn Malaysia for any valuable supports during conducting this research and in preparing this manuscript.

REFERENCES


Taufik was born in Jakarta, Indonesia, in 1969. He received the B.S. degree in electrical engineering from the Northern Arizona University, Flagstaff, in 1990, the M.S. degree in electrical engineering from the University of Illinois, Chicago, in 1993, and the Dr. Eng. Degree from Cleveland State University, Cleveland, OH, in 1999. He is currently an Professor in the Electrical Engineering Department, California Polytechnic State University, San Luis Obispo. His current research interests include the area of power electronics and power system.